

**EE457: Digital IC Design**

**Fall Semester 2016**

**Final Project Report Cover Sheet**

**Due 12/20/16, 6:00PM**

Enigma Game

**PROJECT TITLE: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

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Student Names:\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Enigma

Group Name:\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

***Five points will be deducted for not following the directions***

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| --- | --- | --- |
| Check for completion | Topics | GRADES |
|  | Executive Summary | /5 |
|  | Introduction/Background | /5 |
|  | Circuit Schematics/Symbols | /10 |
|  | Working Electric Layouts | /20 |
|  | IRSIM Logic Simulations | /10 |
|  | Measurements of power/delay/chip area/timing/number of transistors. | /10 |
|  | LTSPICE code/extractions | /10 |
|  | Measurements in LTSPICE | /15 |
|  | Conclusion and References | /5 |
|  | Presentation | …………/10 |
|  | TOTAL | **/100** |

X

We want the chip fabricated and test it through Independent Study. YES\_\_\_\_, NO\_\_\_\_\_

Note: Label your figures and tables. Reference them in your text and discuss your results.

Executive Summary

The name of our project is inspired by the Enigma machine, a cypher machine used by the Germans in World War II. The Enigma machine worked by mapping different letters to each other to produce an encrypted message, which could be sent over the air to another location where it would be decrypted into the original message. This prevented allied forces from being able to understand German communications, unless they could correctly guess the encryption ‘key’ for that day which would correctly decrypt the message. In light of the Enigma machine, our ‘Enigma Game’ requires two players, one to input a ‘key’, and another who must guess it. The rules of the game are as follows:

1. Player 1 and Player 2 agree on a turn limit, which will be the amount of opportunities Player 2 gets to make a ‘guess’.
2. Player 1 inputs a ‘key’ into the designated Player 1 location, consisting of 4 unique extended ASCII or UTF-8 characters (both of which can be represented with 8-bits).
3. Player 2 inputs a guess as to what the key is (which has the same limitations and encoding as the key) into the designated Player 2 location.
4. The ‘Enigma Game’ chip lets Player 2 know whether the characters they guessed exist somewhere in the key and if the character is in the correct location.
5. Player 2 continues to guess and receive a result from the chip until they guess the correct key (meaning all characters in the guess exist in the key and are in the correct place, as determined by the Enigma Game chip), resulting in a win, or the predetermined turn limit is reached, resulting in a loss.

The game chip must only provide two outputs for each character in the guess: the ‘match’ bit which denotes that the character matches one of the characters in the key, and the ‘placement’ bit which denotes that the character is in the correct place. If we limit the key to four characters, the chip can be constructed with a total of eight 8-bit memory blocks (four for the key and four for the guess), sixteen special comparators which can check for a character match, and a 4-bit OR gate to check if there is a match anywhere in the key.

Physical interaction with the game chip would be possible by connecting a keyboard to a decoder which would act as an interface for the character inputs, and lightbulbs or LEDs could be connected to the placement and match outputs for each character so that they are understandable by the players. In order to simulate interactivity with the game chip, we will use the Python scripting language to interface with the Spice model of the device.

Specialized Application-Specific Integrated Circuits (ASIC) such as this one historically have been used in cars and PDAs. Due to the complexity of video games and toys and the specialized nature of ASICs, it’s easier and cheaper for companies to program generic EEPROM chips to run electronic toys or CPUs to run modern video games.

Introduction

Blah

Design and Implementation

Part 1: Blah

Blah

Part 2: Blah

Blah

Part 3: Blah

Blah

Conclusion

Blah

References

Blah